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47390 7590 04/07/2005

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07/11/2005 CNGUYEN1 00000084 10602229

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Hui Chin Barnhill

(Depositor's name)

*J. Ch. Barnhill*

(Signature)

*July 6-2005*

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/602,229	06/24/2003	Shih-Wei Wang	252016-2250	8541

TITLE OF INVENTION: NOVEL DUAL BIT SPLIT GATE FLASH MEMORY

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	07/07/2005
EXAMINER	ART UNIT		CLASS-SUBCLASS		
HUYNH, ANDY	2818		257-315000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

2. For printing on the patent front page, list

- Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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Thomas, Kayden,  
Horstemeyer & Risley  
 3 \_\_\_\_\_

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

## (A) NAME OF ASSIGNEE

Taiwan Semiconductor  
 Manufacturing Co., Ltd.

## (B) RESIDENCE: (CITY and STATE OR COUNTRY)

Hsinchü, Taiwan, R.O.C.

Please check the appropriate assignee category or categories (will not be printed on the patent):  Individual  Corporation or other private group entity  Government

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- b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature Daniel R. McClureDate July 6, 2005Typed or printed name Daniel R. McClureRegistration No. 38,962

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